

DESCRIPTION

ULTRA-WIDEBAND SIGNAL RECEIVER USING FREQUENCY SUB-BANDS

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The present invention relates to a signal receiver and in particular, but not exclusively, to a signal receiver suitable for receiving a wireless signal having a wide bandwidth.

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The use of digital signal processing techniques to implement at least the baseband processing of a wireless receiver can bring benefits such as increased versatility and decreased cost. Commonly, an RF signal is mixed down to a low IF or to baseband before digitisation because digitisation at a high frequency requires a high speed analogue-to-digital converter (ADC). If
15 conventional low-pass sampling is used to sample at the Nyquist rate, an ADC is required that can sample at twice the highest frequency of the signal. The sampling rate required for digitisation at RF may exceed the capability of commercially available ADCs, or require an ADC having a high power consumption or a high cost. Even after mixing down to a lower frequency,
20 sampling at the Nyquist rate for the signal may exceed the capability of commercially available ADCs, or require an ADC having a high power consumption or a high cost.

Ultra-wideband is a technique for performing radio communication and radio positioning which relies on sending a signal comprising ultra-short pulses.
25 Such ultra-short pulses typically occupy frequencies from zero to one or more GHz and it is not practical to use a single ADC which will digitise at the Nyquist rate a signal containing such high frequencies. One solution for reducing the sampling rate below the Nyquist rate for the signal is reported in "Ultra-wideband radar technology", edited by J.D. Taylor, CRC Press, 2001, pages
30 77-78. This solution uses a bank of filters to separate the frequency spectrum of the signal into several sub-bands, mix each sub-band down to DC, and to digitise each sub-band separately using a bank of ADCs, each ADC sampling

at a rate equal to the highest frequency in the mixed-down sub-band. A block schematic diagram of this prior art solution is illustrated in Figure 1 which shows a portion of a receiver suitable for a signal having a spectrum 0-1GHz. The bank of filters, referred to as a channel dropping filter, divides the received
5 signal into five sub-bands each 200MHz wide, and four mixers and four different local oscillator signals are required to bring the signal in all the sub-bands into the range 0-200MHz where a bank of five ADCs, each ADC sampling at 200MHz, is used to digitise the signal in the sub-bands. The digitised output from each ADC is used to reconstruct the signal waveform, although no method
10 or apparatus for doing this is disclosed in the Taylor reference. A disadvantage of this solution is the requirement for a bank of mixers and a means for generating a different local oscillator signal for each mixer. Such a plurality of devices adds to complexity and cost because, for example, mixers are active devices that consume power, contribute noise and have a limited dynamic
15 range.

A further solution is disclosed in "A Channelised DSSS Ultra-Wideband Receiver", Won Namgoong, Proceedings RAWCON 2001, 2001 IEEE Radio and Wireless Conference, Waltham MA, USA, 19-22 August 2001, pages 105-108. This solution uses a bank of mixers each requiring a different local
20 oscillator signal and with each mixer followed by a sub-band filter, so suffers the same disadvantages.

An object of the present invention is to provide a signal receiver that overcomes at least some of the disadvantages of the prior art described above.

According to the invention there is provided a signal receiver comprising
25 digitisation means for digitising a received signal and demodulation means for extracting the information content of the digitised received signal, wherein the digitisation means comprises filtering means for dividing the received signal into a plurality of frequency sub-bands, analogue-to-digital conversion means for digitising the signal in each sub-band, transform means for transforming the
30 digitised signal in each sub-band into the frequency domain, and reconstruction means for concatenating in the frequency domain the digitised signal in each sub-band thereby reconstructing the spectrum of the received signal.

The reconstructed spectrum of the received signal may be at the same frequency as spectrum of the received signal prior to being divided into sub-bands, or may be at a lower frequency, for example a received signal having a bandpass spectrum may be shifted to DC.

5 By dividing the received signal into a plurality of sub-bands and digitising the signal in each sub-band, the requirement for an ADC capable of digitising the whole signal bandwidth by operating at the Nyquist rate is avoided. By digitising each sub-band without first downconverting each sub-band, the requirement for a bank of mixers and means for generating a different local
10 oscillator signal for each mixer is avoided.

Optionally the received signal, if a bandpass signal, may be down-converted from a transmission frequency to a lower frequency prior to the digitisation means.

The sample rate required for digitising the signal in a sub-band depends
15 on the bandwidth of that sub-band, as described below.

By judicious selection of sample rate for each sub-band, it is possible to use a common sample rate for a plurality of sub-bands, thereby simplifying the sample rate clock generation. Such sub-bands may have a common bandwidth.

20 By judicious selection of sample rate and bandwidth for each sub-band, it is possible to use a common sample rate for odd numbered sub-bands and a different common sample rate for even numbered sub-bands thereby simplifying the sample rate clock generation to two rates.

The analogue-to-digital conversion means may comprise an ADC for
25 each sub-band. However, if the receiver is to be used to receive a signal that occupies only one sub-band at any one instant, such as a frequency hopping signal or a chirp signal, the analogue-to-digital conversion means need only digitise one sub-band at a time and a single ADC can be switched to each sub-band in turn, tracking the frequency of the received signal, thereby reducing the
30 complexity of the analogue-to-digital conversion means.

The invention will now be described, by way of example only, with reference to the accompanying drawings wherein:

Figure 1 is block schematic diagram of a prior art receiver,

Figure 2 is a diagram of the frequency response of a filter bank,

5 Figure 3 is block schematic diagram of a first embodiment of a wireless receiver in accordance with the invention,

Figure 4 is block schematic diagram of a second embodiment of a wireless receiver in accordance with the invention, and

Figure 5 is a sketch illustrating sub-band spectra.

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Referring to Figure 3 a first embodiment of the invention comprises a signal input 5 for receiving a signal from an antenna. Coupled to the signal input 5 is a low pass filter 10 for removing unwanted signal components from the received signal. Coupled to the output of the low pass filter 10 is an
15 amplifier 20. The output of the amplifier 20 is coupled to an input of a filter bank 30. The filter bank 30 divides the signal delivered by the amplifier 20 into five sub-bands and the signals in each sub-band are delivered to respective ADCs 41-45 where they are digitised. Coupled to an output of each ADC is a respective FFT means 51-55 for transforming the digitised sub-band signal to
20 the frequency domain, illustrated by the sketch 59 of a sub-band spectrum, using a Fast Fourier Transform (FFT). Coupled to an output of each FFT means 51-55 is a respective frequency shifting means 61-65 which performs a shifting of the frequency of the frequency domain sub-band signals to be positioned at DC, illustrated by the sketch 69 of a shifted sub-band spectrum,
25 unless already at DC, by re-labelling of the frequencies. Coupled to an output of each frequency shifting means 61-65 is a respective storage portion 71-75 of a storage means 70. The frequency shifted sub-band signals are concatenated by storing them in their respective storage portions 71-75, thereby reconstructing the received signal in the frequency domain. An output of the
30 storage means 70 is coupled to a first input of a multiplier means 80 which multiplies the reconstructed received signal by a reference signal, the reference signal being a replica of the signal spectrum as transmitted, being stored in a

reference signal store 85 and being delivered to a second input of the multiplier means 80. An output of the multiplier means 80 is coupled to a means 90 for performing an Inverse Discrete Fourier Transform (IDFT) which provides on an output 95 the cross-correlation function of the received signal and the reference signal. The output 95 is coupled to a processing means (PROC) 96 for further processing of the cross-correlation function as required by the application for which the wireless receiver is to be used. For example, the time of occurrence of a peak in the correlation function may be measured to determine the flight time of the signal, and consequently the range of the transmitter from the receiver. As another example, the polarity of a peak in the correlation function may be determined to detect the value of a data bit conveyed by the signal. As a further example, modulation of the time of arrival of the received signal may be determined as conveying information.

Figure 2 shows the frequency response of a filter bank which divides a signal having a bandwidth f_b into N sub-bands. In Figure 2, each sub-band has the same bandwidth f_b/N but this is not essential. For the embodiment shown in Figure 3, $N=5$.

If the signals in the sub-bands are to be sampled by the respective ADCs 41-45 without aliasing, the ADC sampling rates must be selected to satisfy the following inequality:

$$\frac{2f_{u_i}}{r_i} \leq f_{s_i} \leq \frac{2f_{l_i}}{r_i - 1} \quad (1)$$

$i = 1, \dots, N$, where f_{s_i} is the sample rate for the i^{th} sub-band, f_{u_i} is the upper frequency limit of the i^{th} sub-band, f_{l_i} is the lower frequency limit of the i^{th} sub-band, and r_i is an integer satisfying the inequality $1 \leq r_i \leq \text{int}\left\{\frac{f_{u_i}}{f_{u_i} - f_{l_i}}\right\}$. In

general, it is preferable that ADC sampling rates are selected such that aliasing is avoided, although for some types of received signal an amount of aliasing may be tolerable. In the following description it is assumed that aliasing is avoided.

Optionally r_i may be selected such that a plurality of sub-bands share a common sample rate, which simplifies sample rate clock generation. For example, for a 5-sub-band embodiment as shown in Figure 3, and denoting the bandwidth of the i^{th} sub-band as W_i , where $W_i = f_{u_i} - f_{l_i}$, gives

$$5 \quad \text{sub-band 1: } 2W_1 \leq f_{s1} \leq \infty \quad \text{for } r_1=1, \quad (2)$$

$$\text{sub-band 2: } 2(W_1 + W_2) \leq f_{s2} \leq \infty \quad \text{for } r_2=1, \quad (3)$$

$$\text{sub-band 3: } (W_1 + W_2 + W_3) \leq f_{s3} \leq 2(W_1 + W_2) \quad \text{for } r_3=2, \quad (4)$$

$$\text{sub-band 4: } (W_1 + W_2 + W_3 + W_4) \leq f_{s4} \leq 2(W_1 + W_2 + W_3) \quad \text{for } r_4=2, \quad (5)$$

$$\text{sub-band 5: } \frac{2}{3}(W_1 + W_2 + W_3 + W_4 + W_5) \leq f_{s5} \leq (W_1 + W_2 + W_3 + W_4) \quad \text{for } r_5=3 \quad (6)$$

10 In this case, a common sample rate satisfying inequality (3) may be used for sub-bands 1 and 2.

Other possibilities of common sampling rates may be determined for specific values of r_i and sub-band bandwidth W_i .

Sub-bands sharing a common sample rate may optionally have a
15 common bandwidth. For example, if $W_1 = W_3 = B$, a common sampling rate can be used for sub-bands 1 and 3 provided that it satisfies the inequality $3B \leq f_3 \leq 4B$. As another example, if $W_2 = W_4 = B$, a common sampling rate can be used for sub-bands 2 and 4 provided that it satisfies the inequality $4B \leq f_4 \leq 6B$.

20 Particularly advantageous sample rates can be derived by setting

$$r_i = \left\lfloor \frac{i+1}{2} \right\rfloor \text{ and } W_i = B \text{ for all values of } i, \text{ i.e. a common bandwidth for all sub-}$$

bands. In this case only two different sample rates are required; a first common sample rate may be used for all the odd numbered sub-bands, provided that it satisfies the inequality (1) for the highest odd-numbered sub-band, and a
25 second, different common sample rate may be used for all the even numbered sub-bands, provided that it satisfies the inequality (1) for the highest even-numbered sub-band. For the embodiment shown in Figure 3 and a common bandwidth of $B=200\text{MHz}$ for all sub-bands, the first common sample rate must

satisfy the inequality $\frac{10}{3}B \leq f_{s_i} \leq 4B$, and so a value of 733MHz would conveniently lie in the centre of the allowable range, and the second, different common sample rate must satisfy the inequality $4B \leq f_{s_e} \leq 6B$, and so a value of 1GHz would conveniently lie in the centre of the allowable range.

5 Other possibilities of a first common sampling rate for the odd-numbered sub-bands and a different common sample rate for the even numbered sub-bands may be determined for specific values of sub-band bandwidth W_i where the sub-band bandwidths are unequal.

The acceptable range of values of the sample rate as expressed by the
 10 inequality (1) defines the acceptable limits of frequency error that each sample rate may have. It is preferable to select sample rates that lie approximately in the centre of their respective acceptable range. For the embodiment shown in Figure 3 and with $B=200\text{MHz}$, the tolerance on a first common sample rate of 733MHz is approximately $\pm 9\%$, and the tolerance on a second common sample
 15 rate of 1GHz is $\pm 20\%$. Alternatively, for a chosen sample rate f_{s_i} , the inequality (1) can be used to define the acceptable tolerance limits on f_{l_i} and f_{u_i} for the respective sub-bands provided by the filter bank 30. Of course the tolerance on the sample rate may be traded for tolerance on f_{l_i} and f_{u_i} .

The digitised sub-band signals delivered by the ADCs 41-45 include
 20 copies of the sub-band spectrum replicated at integer multiples of the sample rate. Therefore, the FFT means 51-55 comprise filtering means for selecting a single sub-band spectrum. Figure 5 illustrates sub-band spectra (amplitude A versus frequency f) for the example of a 0-1GHz signal divided into sub-bands 200MHz wide.

25 Figure 5 plot 5(a) illustrates the first sub-band spectrum at 0-200MHz with a sample rate of $f_{s_i}=733\text{MHz}$. Replica spectra are all above 200MHz and are filtered out by the FFT means 51; they are not shown in plot (a).

Plot (b) illustrates the second sub-band spectrum at 200–400MHz with a sample rate of $f_{s_2}=1\text{GHz}$. Replica spectra are all above 400MHz and are filtered out by the FFT means 52; they are not shown in plot (b).

Plot (c) illustrates the third sub-band spectrum at 400-600MHz sampled at a sample rate of 733MHz. The sampling process generates a replica of the sub-band spectrum at 133-333MHz, reversed such that the lower frequencies of the sub-band prior to sampling now appear as the upper frequencies of the replica at 133-333MHz. In Figure 5, the envelopes of the replica sub-band spectra are indicated with a broken line. The FFT means 53 comprises means for selecting the replica sub-band spectrum at 133-333MHz and also for reversing its spectrum to restore the order of its frequency components.

Plot (d) illustrates the fourth sub-band spectrum at 600-800MHz sampled at a sample rate of 1GHz. The sampling process generates a replica of the sub-band spectrum at 200-400MHz, reversed such that the lower frequencies of the sub-band prior to sampling now appear as the upper frequencies of the replica at 200-400MHz. The FFT means 54 comprises means for selecting the replica sub-band spectrum at 200-400MHz and also reversing its spectrum to restore the order of its frequency components.

Plot (e) illustrates the fifth sub-band spectrum at 800-1000MHz sampled at a sample rate of 733MHz. The sampling process generates a replica of the spectrum at 67-267MHz without any reversal. The FFT means 55 comprises means for selecting the replica sub-band spectrum at 133-333MHz.

The frequency shifting means 61-65 are used to shift each selected sub-band spectrum to DC. For the example, as illustrated in Figure 5, the shift required by sub-bands 2, 3, 4 and 5 are respectively 200MHz, 133MHz, 200MHz, and 67MHz. The shifting can be achieved by re-labelling the frequency of the spectral components. Optionally, the reversing of replica sub-band spectra may be performed by the frequency shifting means instead of by the FFT means.

The received signal is reconstructed in the frequency domain by concatenating the frequency shifted sub-band signals in the storage means 70. The process of concatenation shifts the i^{th} sub-band signal, $i=2, N$, in the

frequency domain to respective frequencies f_i , $i=2, N$ which in the example illustrated in Figure 5 are 200, 400, 600 and 800MHz. The reconstructed spectrum is illustrated in Figure 3 by the sketch 79 of the spectrum delivered at the output of the storage means 70. The resolution of the FFT means 51-55 depends on the sample rate of the respective ADC 41-45; a high sample rate results in frequency components more closely spaced than a lower sample rate. Because the resolution of each the FFT means 51-55 is not equal, the frequency components of the reconstructed spectrum are not uniformly spaced. This non-uniformity is not illustrated in the sketch 79 of the reconstructed spectrum. The reference signal stored in the reference signal store 85 is specified at the same non-uniformly spaced frequency values as the reconstructed spectrum. The means 90 for performing an IDFT is able to operate with the non-uniform spaced frequency values.

Referring to Figure 4 which illustrates a second embodiment of the invention, identical reference numerals have been used for elements that are identical or similar to elements of the embodiment illustrated in Figure 3; the differences of elements of Figure 4 are described below. The embodiment illustrated in Figure 4 can be used if the receiver is to be used to receive a signal that occupies only one sub-band at any one instant, such as a frequency hopping signal or a chirp signal. A single ADC 41, FFT means 51 and frequency shifting means 61 is used. The input of the single ADC 41 is switched to each output of the filter bank 30 in turn by means of a first commutating switch means 100, and output of the single frequency shifting means 61 is switched to each storage portion 71-75 in turn by means of a second commutating switch means 101. The switching of the first and second commutating switch means 100, 101 is synchronised by a synchronisation means (SYNC) 99. The order in which sub-bands are selected for processing is predetermined to match the known frequency hopping sequence or known chirp profile of the transmitted signal. A clock generator (CLK) 98 generates the sample rates required by the ADC 41 for digitising each sub-band in turn and a selection switch means 102 is synchronised by the synchronisation means 99 to select the required sample rate for each sub-band. The selection switch

means 102 in Figure 4 provides for selection between two sample rates, but any required number of selections may be provided. The synchronisation means 99 is also coupled to the FFT means 51 to synchronise the switching of the filtering and reversing functions as appropriate to the current sub-band signal.

If the switching of the first and second commutating switch means 100, 101, the selection switch means 102, and the filtering and spectrum reversing of the FFT means 51 are not synchronised to the sub-band which the received signal occupies at any one instant, after one commutation cycle the storage means 70 will not contain a complete set of sub-band signals and so the received signal will not be fully reconstructed in the storage means 70 and the cross-correlation function provided at the output 95 will exhibit weak correlation. The output 95 is coupled to an input of the synchronisation means 99 which adjusts the phase of the commutating switch means 100, 101, the selection switch means 102, and the filtering and reversing of the FFT means 51 until a maximum correlation is exhibited at the output 95. Optionally, means for detecting the signal strength in each sub-band may be included to provide an indication to the synchronisation means 99 of the current frequency occupancy of the received signal, thereby assisting the synchronisation means 99 to synchronise the commutating cycle, the selection switch means 102 and the FFT means 51 with the received signal.

Although the invention has been described by means of an example of a wireless receiver, the invention is equally applicable to a receiver for receiving a signal via a different medium, for example via wire or optically.

Although the invention has been described by means of an example of a receiver suitable for receiving a wide-band or ultra-wideband signal, the invention can also be used for receiving signals having a narrower bandwidth.

Optionally a demodulation process different to that described herein may be applied to demodulate the digitised received signal.

Optionally the receiver may comprise a power saving scheme in which some or all of the receiver elements adopt a power saving mode and are activated at intervals to receive a signal. For example, if the receiver is to be

used to receive a signal having a duty cycle less than one, then a bank of ADCs can also be operated with a duty cycle less than one, sampling only for periods in which the signal is expected to be present.

5 In the present specification and claims the word "a" or "an" preceding an element does not exclude the presence of a plurality of such elements. Further, the word "comprising" does not exclude the presence of other elements or steps than those listed.

10 From reading the present disclosure, other modifications will be apparent to persons skilled in the art. Such modifications may involve other features which are already known in the design, manufacture and use of signal receivers, and which may be used instead of or in addition to features described herein.